UNITED STATES PATENT AND TRADEMARK OFFICE

Facsimile Transmission

To:	**Name:**	Ryan Flax
	Company:	
	Fax Number:	1-202-379-9113
	Voice Phone:	
From:	**Name:**	Paul Berardesca
	Voice Phone:	571-270-3579

37 C.F.R. 1.6 sets forth the types of correspondence that can be communicated to the Patent and Trademark Office via facsimile transmissions. Applicants are advised to use the certificate of facsimile transmission procedures when submitting a reply to a non-final or final Office action by facsimile (37 CFR 1.8(a)).

Fax Notes:

Revised Unofficial Examiner's Amendment for application 10/661,494 (Attorney Docket No.: M4065.0087/P087-A). Please respond to Paul Berardesca (tel. 571-270-3579) as soon as possible.

Date and time of transmission: Wednesday, January 27, 2010 2:00:14 PM
Number of pages including this cover sheet: 04

UNOFFICIAL EXAMINER'S AMENDMENT

113. (Currently Amended) An imager structure, comprising:

a pixel array having pixels arranged in rows and columns;

said pixel array comprising a first pixel and a second pixel formed in respectively adjacent columns and in conjunction with an active area spanning a first associated photodetector of the first pixel and a second associated photodetector of the second pixel but no other photodetectors of other pixels, said active area having the first associated photodetector and the second associated photodetector at opposite ends of said active area and having a two-dimensional shape as the pixel array is viewed from above; and

a common output for charges generated from the first photodetector and the second photodetector at a portion of said active area between said first and second photodetectors, the common output being coupled to a signal output line shared by the first pixel and the second pixel, wherein the two-dimensional shape of said active area between the first and second associated photodetectors comprises two substantially parallel line segments which are diagonal ~~is a substantially diagonal shape~~ with respect to an extending direction of said signal output line ~~column line~~ within the pixel array.

114. (Currently Amended) The pixel array of claim 113, wherein the active area ~~shared by the first and second pixels~~ is S-shaped.

115. (Currently Amended) A pixel array, comprising:

a first pixel and a second pixel, the first pixel having a first photodetector and the second pixel having a second photodetector, wherein an active area is shared by only the first photodetector ~~shares an active area with~~ and the second photodetector ~~and no other photodetector~~, said shared active area providing an output for said first and second photodetectors and having a two-dimensional shape as the pixel array is viewed from above; and

a common readout line for receiving a signal from said first and second photodetectors coupled to the active area shared by the first pixel and the second pixel, wherein the two-dimensional shape of the shared active area between the first and second photodetectors comprises two substantially parallel line segments which are diagonal ~~is a substantially diagonal shape~~ relative to an extending direction of the common readout line across the pixel array.

117. (Currently Amended) A pixel array, comprising:

a first pixel and a second pixel, said first pixel having a first photodetector and said second pixel having a second photodetector, wherein an active area is shared by

only said first photodetector ~~shares an active area with~~ and said second photodetector, ~~and no other photodetector~~ and at least one of said first pixel and said second pixel further comprises a reset transistor, said reset transistor comprising a gate, a first source-drain region, and a second source-drain region in a linear arrangement as viewed from above the pixel array, wherein said shared active area between the first and second photodetectors has a ~~substantially diagonal~~ two-dimensional shape as the pixel array is viewed from above, the two-dimensional shape comprising two substantially parallel line segments which are diagonal relative to the linear arrangement of the reset transistor as the pixel array is viewed from above; and

a common readout line for receiving charge from said shared active area.